

Method based on an algorithm capable of being graphically implemented to be used for the generation or filtering of data sequences and cryptographic applications

5 Scope of the invention

The present invention consists in a method based on an algorithm conceived for graphic implementation to be used either for generating or filtering data sequences, and
10 specially apt for cryptographic applications, following the exchange of a symmetric secrete key code randomly generated to be exchanged through a secure channel between the sender(s) and the recipient(s), the key will be the same for the encryption and decryption process, i.e. this is a symmetrical
15 key.

The examples which will be provided of such algorithm are included in these Technical Specifications under references G1000, G2000 and G2000i, and belong to the technical
20 area of graphic computerised processes at pixel scale (raster images) which may be considered as automaton algorithms of fractal behaviour.

On the other hand, the encryption/decryption methods
25 based on the algorithms referenced as E-G2000 and E-G2000i belong to the technical field of data protection and secure telecommunications, that is, to the sphere of cryptography. Under methods E-G2000 and E-G2000i, both the encryption and the sequence generator make use of the graphic algorithms
30 G1000, G2000 and G2000i, created by the inventor. Although all these algorithms work with variable length blocks they may be used as flow ciphers since the operations performed by them are simple enough to be executed with more than enough speed by these applications. Moreover, as they are based on graphic
35 algorithms, they benefit from all the technological advances which are currently taking place in the field of multimedia

graphics and hardware acceleration. These algorithms open the door to a new device which making use of the GPU capabilities, combines graphic and cryptographic functions at low cost, and therefore this device would have a great demand providing easy cryptography (without loading the CPU) to PC (Personal Computers) all over the world guaranteeing secure communications conducted either in any intranet or in the world-wide net.

10 Background of the invention

The algorithms provided by the invention, referenced as G1000, G2000 and G2000i belong to the group of automaton algorithms, which likewise includes cell automata, the Langton ant, etc. Notwithstanding, all these processes within the current state of the art have their own specific characteristics and have no bearing with this invention.

Regarding the encryption/decryption processes based on such algorithms known as E-G2000 and E-G2000i to this date and to the knowledge of the inventor there are not encryption algorithms based on any automated graphic algorithm.

Description of the invention

25

These Technical Specifications provides a definition as well as the basic framework for the graphic algorithms G1000, G2000 and G2000i together with the cryptographic applications of the last two algorithms, with descriptions and operating diagrams of them.

30

The algorithms G1000, G2000 and G2000i are graphic processes conducted at pixel level, which could be considered as an automaton of fractal behaviour.

35

On the other hand, methods E-G2000 and E-G2000i are based on symmetrical encryption algorithms of variable length blocks, supported by a Pseudo-noise sequence generator (or two in the case of the E-G2000i), based on one (or two) linear
 5 sequence generators (LFSR with a primary polynomial).

Graphic algorithm G1000 may be considered as simple automaton algorithm of fractal behaviour.

10 It basically consists in plotting a set of lines which is defined by a pole and a contour (that is, the set of lines linking that pole with all other contour points), in which an operation is performed with the pixels used in each line (the pixels are selected by a Bresenham algorithm or a similar
 15 one), so that, for example, the pixels in the inside of the contour invert their state when plotted, each time that pixel in question belongs to a line within the set. In other words, in the event that a pixel may only have two states (ciphering by bits), on drawing a pixel which is in state 0, the latter
 20 changes to state 1, and if it is in state 1, it changes to 0 (Logic Xor), operation which is equivalent to a modular addition:

Final state (Initial state + $n/2$) mod n

25

Where n is the number of possible states for any given pixel. Thus, the graphic algorithm meets the property of being symmetrical (in the sense that it is the reverse of itself, in other words, if we apply the same twice with the initial
 30 screen in blank, we obtain a blank screen again) thus conferring symmetry to the encryption algorithm.

Usually, an algorithm so defined does not require many parameters for its implementation; for example, in the event
 35 of a rectangular contour, it only needs the size of the rectangle and the position of the set's pole. Figure 1 of the

drawings attached, shows the results for this algorithm applied to rectangular contour of 200x160 pixels with the pole set at (100,80), and the initial screen in blank.

5 On defining the algorithm as above, a generation routine would have the following appearance:

```
For (x,y) = (first contour pixel) To (last pixel)
    Plotted line (Xpole,Ypole, x,y)
```

10 Next (x,y)

Please note that with this simple routine a highly complex result is achieved, as the position of each pixel depends on the number of times it has been drawn. Those pixels subject to an even number of lines remain in their initial position, whereas those subject to an odd number of lines remain in an inverted position.

20 Please note that an algorithm so defined may be applied to a great number of multidimensional vector spaces or even one-dimensional; the contour may be as simple as a defined segment of a line, the pole, a pixel of the line, internal or external to the bounded segment; and the set of lines, those which are plotted from that pole to all discrete points of the

25 bounded segment.

This algorithm, which we refer to as G1000 as has been defined up to now would hardly be applicable for cryptography purposes, as the contour pixels only undergo one change of state, wherefore the final result is exactly the reverse of their initial state.

Therefore, the method of the EG2000 and EG2000I ciphers is based on a more complex algorithm, referred to as G2000 which makes use of two contours instead of one; one external, which together with the pole defines the set of lines, and another internal which establishes the area of interest where data will be loaded or stored (for example, information to be encrypted, or a number of values to be filtered), as may be seen in the diagram of Fig.2, and it must be taken into account that the pole does not have to be located within the area of interest and generally, it will be located in the second contour (although not necessarily) and to a greater advantage close to one or both contours (preferably the second one), and the set lines may cover either all or part of the first contour.

By introducing this amendment, the weakness pointed out above is overcome by simply introducing a slight increase in complexity (please note that any implementation of the foregoing algorithm, would only have to calculate the result for the area of interest, even if the set of lines is defined by the external contour). This amendment likewise entails an increase in the number of parameters involved; if initially there were four (T,R,Xpole,Ypole), now we have eight (T, R, Xpole, Ypole, Ix, Rx, Iy, and Ry) which in principle are independent from each other, although we shall later insist on the need for Ix, Rx, Iy and Ry to be dependent in order to ensure the strength of algorithm G2000 for cryptographic uses.

G2000 is provided with extremely favourable properties for its cryptographic application:

5 1. It may be carried out against any background, whether a cluster of data to be ciphered, or a sequence stream generated by Linear Feedback Shifted Registers (LFSR), without filtering; thus it may be used as an encrypter, filter, sequence generator, etc.

10 2. It may be repeated any number of times desired by simply changing the pole or any other of the parameters, thus providing a greater output complexity.

15 3. Its symmetry makes it the inverse of itself; in other words, if against a specific background a certain number of G2000 cycles are conducted, whether complete or not, on repeating these on the result (in any order), the initial background is obtained. In fact, this symmetry is conferred by the Logic Xor operation, whereby it is possible to conceive
20 other functions which being symmetrical are not the one described. In fact, even the use of non-symmetrical functions may be envisaged for their application in key exchange protocols, or HASH algorithms, and in general, non symmetrical
25 encryption methods, or digital signatures.

Encryption/decryption method referred to as E-G2000 is based on the three properties of the G2000 mentioned above.

30 Most of the disadvantages which may be observed in the G2000 for its cryptographic operation arise from the use of rectangular contours when defining the set of lines plotted, introducing a certain pattern regularity, which is small but undesirable, notwithstanding. Therefore as a subsequent
35 alternative, algorithm G2000i is proposed, as it generates a set of lines by means of irregular contours. These contours

are defined by lines, but these lines do not present a regular pattern.

For example, in an operational alternative of the
 5 G2000i, the second contour which encompasses a first contour
 for the data area, relies on the output of a filtered (LFSR),
 so that the distance from the final points of each line to
 their pole in question, depends of such output, with the only
 caution that the lines must be adjacent in some of their
 10 external points to the area of interest, so that two
 consecutive lines may share common points and therefore not
 only a mere inverted image is obtained, ensuring maximum
 complexity. For additional accuracy, figure 3 is herein
 provided, in which D is a distance which depends on the output
 15 of a Pseudo-Noise Sequence Generator, which may be interpreted
 positive or negative, but its final point never is found
 within the area of interest. The Adjacent Point, will be
 located below, to the left, above or to the right of the
 preceding one, according to whether its link line to the pole
 20 crosses the contour of the area of interest to the right,
 below, to the left or above, respectively.

Regarding the cryptographic applications of the above
 method, it must be noted that the chief aim of any
 25 cryptographic algorithm is undoubtedly to destroy any
 statistical link with the data to be ciphered, so that its
 output resembles as closely as possible a random noise and
 there is no possibility of establishing a statistical or
 algorithmical relationship between the cryptograms and the
 30 original data, which will be referred to in these Technical
 Specifications as "plaintext", unless the cipher key is
 available.

Likewise the space for the keys must be sufficiently
 35 ample to eliminate the possibility of an attack by brute force
 (exhaustive testing of all the keys). This requirement is more

than fully met by the E-G2000 herein described, as the key space is of $7 \cdot 10^{50}$, sufficiently large to make it unthinkable that a machine could try them all within a reasonable period of time. Moreover, should it be required, there are no limitations to increasing the length of the key, as it will suffice to increase the LFSR degree used in generating the linear sequence and which is part of the definition of the key itself.

On the other hand, output cryptograms must present a maximum dispersion and a frequency distribution closely resembling a random noise signal, regardless of the input redundancy (in the plaintext). According to this, each character must appear once every n times, n being the number of characters with a probability of appearing. In IT communications, the initial starting point is 256 characters with a probability of appearing, therefore each of these should be present once in every 256 characters sent; likewise each pair of characters possible should be present once in every 65,536 (256^2), each three characters once every 16,777,216 (256^3)... and so on. Likewise the average distance between identical characters should be of 256, between pairs 65,536,... between groups of three, 16,777,216 ...etc. Obviously all the above must be met, but always in terms of probability and not literally.

Brief description of the drawings

Fig 1 is a diagram which shows the results of applying an algorithm, such as G1000, previously mentioned, to a rectangular contour.

5

Fig 2, is a diagram explaining the definition and structure of the algorithm G2000, showing the two contours used, the position of the pole and the way to plot from that pole the set of lines originating from the same, subsequently based on the second contour.

10

On the other hand, Fig. 3 shows an example of an unregularized second contour, using the algorithm G2000i, together with the results obtained by the example provided.

15

Figure 4 is a flow chart of a preliminary version for an encryption/decryption method based on the algorithm G2000.

Figure 5 shows an example of a possible valid key structure for the application of method E-G2000.

20

And Figure 6 is a flow chart which provides a most developed version of such a method for the encryption and decryption of data using a random key structure such as that shown in Figure 5.

25

Detailed explanation of several examples concerning the implementation of the invention method.

30

In essence, the method of the invention is based on an algorithm subject to being implemented graphically both for the generation and filtering of data streams and cryptographic applications, and basically includes the following stages:

a) Defining a cell array distribution with a computer referenced to a system of coordinates in a vector bidimensional space, provided that the cells in question are capable of adopting at least two states.

5

b) defining a first area within that bidimensional vector space, bordered by a first contour, using part of the said cells to define the successive points of this first contour and including a certain number of those cells in this first area;

10

c) defining a second area in that bidimensional space bordered by a second contour using part of the cells to define the subsequent points of the same; this second area contains the first area;

15

d) choosing a cell as the pole, and successively plot a set of lines from this pole, to a given number of the cells which define the second contour, covering all or part of that contour until the first area has been fully swept, using for each line the cells determined by a plotting device such as a Bresenham algorithm; and

20

e) performing, on the contents of each of the cells used when plotting each of the lines of the set and included in that first contour, an operation such as a (Logic Xor), that transforms their state, each time the cell in question is found in one of the lines of the set.

25

In the examples set forth in these Technical Specifications, it is understood that such bidimensional space has materialized in a discrete plane similar to the computer screen, considering each cell as a pixel or basic element of an image or cluster of data. It is important to point out that such discrete plane and all the elements required for the application of the foregoing algorithms may be represented in

35

a purely analytical manner, and not necessarily by graphic means.

Figure 2 shows the basic elements to implement these stages.

5

In a preferred embodiment of such method, at least the second contour is irregular and its cells are obtained from a Pseudo-Noise Sequence Generator, so that the distance of the cells of this second contour to their pole depends on the output of the

10

The method for its applications in filtering and cryptography includes likewise a previous stage d1) prior to e) which consists in assigning the successive values of a data block with a certain length, or undetermined, to be encrypted or filtered, associating them in a pre-arranged manner to the cells of the said array delimited by the first contour. Both this data link, their storage and extraction must be undertaken in such a manner to ensure that certain data bits remain during the decryption process in the same relative position in reference to those contours as the one they had during the encryption process. Thus, it is ensured that each bit undergoes a transformation leading it to its original state. This fact must be considered in the reading/writing operations of encrypted or plain data stored by different procedures.

25

The method E-G2000 which will be described purely by way of example and which is set out by means of a flow chart under Figure 4 of the attached drawings is a symmetrical encryption cipher algorithm of different block lengths, supported by a Pseudo-Noise Sequence Generator (PNSG), both of these based on a graphic algorithm G2000 whose defining elements have been set out in Figure 2, or on G2000i with unregularized points included in the second contour pursuant to Figure 3 and explanations provided thereon. In the Pseudo-Noise Sequence

30

35

Generator (PNSG), the algorithm G2000 is used as a filter of the output sequence of a LFSR, considering the latter in variable length blocks, depending on a key (K), (whose structure shall be more fully described hereinbelow), the first time and subsequently relying on the output sequence of the filtered generator. In the encryption procedure notwithstanding, the G2000 is carried out by a modular addition with the sequence of plain data. Thereby, the generated cryptograms do not require any control character or parameter apart from K, which makes them apt for the encryption of any means of communication or file, from text, executable files, or libraries, without any limitation whatsoever regarding the type of data or the operating system. On the other hand, the cryptograms generated are in fact random signals with highly complex statistical distributions independent from the input redundancy and the only information which the algorithm leaves plain after the cipher process, is the length of the message sent, which on the other hand may be easily altered by the sender, if so required.

20

Please refer to the flow chart of the E-G2000 which is shown in Figure 4; the method therefore is executed according to the following stages:

- 25 1. At the very beginning, the E-G2000 initializes a PNSG with the initial parameters obtained from the K key, thus the first rotation of the sequence generation takes place, obtaining a LFSR array sequence without filtering, to which immediately after and using parameters extracted from K, the
30 algorithm G2000 applies a specific number of rotations, thereby destroying the linear relations in the sequence extracted from the LSFR and obtaining a highly complex Pseudo-noise sequence. Such complexity may adopt different degrees, as it depends on the LFSR period and the fact that the output
35 sequence may be periodical or not. To the purposes of filtering the sequence, this process may be undertaken in

combination with a sequence of the LFSR itself taken later on or with a second LFSR applied in a similar way to the G2000 in the rotation of data ciphering, that is, performing a modular addition between the bit which belongs to the array we are
5 filtering and the corresponding one from the second LFSR or the same LFRS later on (although this last process of combining an LFSR with its own is not advisable). Therefore a considerable increase in the output complexity is obtained.

10 2. Once the parameters have been obtained from the sequence generated, the first array with clear data (data from the original file to be encrypted, introduced in the array pursuant to a specific order and without any modifications) is prepared and a G2000 is applied in modular two addition with
15 the aforementioned sequence. This step and the previous one can be repeated any number of times with different parameters, to reinforce the ciphering. In the location of the clear data of the array to be encrypted, it is advisable to introduce a transposition dependant on the key, for example a pseudo G2000
20 transposition may be applied, that is, instead of filling this data array by rows or columns directly taken from the original file, it may be filled radially from a pole extracted from PNSG), avoiding the overlapping of data, whereby these occupy positions in the plain-data array which have not yet been
25 occupied (in some applications overlapping is acceptable, for example when using the algorithm as a HASH function), and of course, in the decryption process exactly the reverse operation must be carried out. Thus, the E-G2000 becomes a transposition algorithm as well as a substitution algorithm,
30 which makes it even safer still. If this cautionary measure is not adopted, then it is mandatory to relay the message with its HASH summary, or any other kind of authentication or signature to guarantee the method's security.

35 3. Once step 2 has been conducted, the initial clear array is already encrypted and ready to be stored or relayed

by any data processing or sending. The E-G2000 continues operating, extracting a new plain data array and repeating the same routine, but now only using the sequence from the PNSG both when obtaining parameters and ciphering, and not the bits of the K Key.

4. It may happen that at any moment during encryption the sequence previously generated is completed, and it will become necessary to predict the end of the sequence with $n \cdot m$ bits in advance as these are required by the PNSG in order to be used as parameters when filtering the LFSR sequence. Where n is the number of rotations of the G2000 which make up the linear sequence filtering and m the number of bits required in order to generate the parameters in each rotation.

It must be stressed that even though the algorithm could be described in two clearly different parts, to wit, the PNSG and the G2000 applied to the data combined with the sequence, these stages are extremely similar to each other, whereby in practice it becomes possible to implement single subroutines which perform the two stages, which considerably simplifies and introduces cost savings in the implementation of the algorithm whether via software or hardware.

The method and structure abovementioned implies several advantages such as:

a. It allows for the use of only one of the sections of the E-G2000 to reinforce current cryptographic systems. Any encryption algorithm which requires a PN sequence generator, could replace the one originally implemented by a PNSG based on the G2000 or the G2000i, thus obtaining an enhanced complexity in the output sequence, which leads to a greater encryption security at a lesser cost than that arising from replacing the whole encryption system.

b. It may easily benefit from parallel process architectures and specially of multiprocessor systems as one of the processors may be wholly devoted to the generation of the sequence while another one is entirely devoted to the application of the G2000 of G2000i to the data.

c. In monoprocessor systems which are not so sophisticated, it must be considered that they are almost always provided with a graphic processor, which due to the graphic nature of the E-G2000 and the accelerated evolution of these devices, may become an invaluable ally. Undoubtedly, the recent GPU devices recently available in the market are of great help.

d. Its symmetrical nature implies that operations undertaken in the encryption process are exactly the same as in the decryption process, whereby its implementation is greatly simplified as there is no need to establish different routines for each case.

A valid key for the application of the E-G2000 method is any random stream with a certain length of bits (in this case 169), notwithstanding there are a priori streams considered better than others for a simple reason, to wit, because the LFSR definition of n degree used to generate the linear sequence is part of the key itself. This LFSR (Linear Feedback Shifted Register) is defined by a polynomial of binary coefficients and an initial seed, both of the same length (in this case 63 bits) which generate a periodic binary sequence, and its period depends on whether the polynomial may be factored, or is irreducible or primitive. In the first case the period (P) depends on the initial seed and is less than $2^n - 1$, in the second P does not depend on the seed, but is a $2^n - 1$ divider and in the third case P not only does not depend of the seed but is also a maxima and therefore equal to $2^n - 1$. Due to this, the most desirable polynomials are the primitive

ones, and when defining K this condition should be met. Notwithstanding, K may be defined in a purely random manner and one may choose the first subsequent or preceding primitive polynomial to that included in K as the LFSR definition; in
 5 that case key space is reduced, but does not entail any hazard and therefore a non expert user may disregard the primitive nature of the polynomial, thus reinforcing the LFSR output. The K structure could remain as shown in the example under
 10 Figure 5 and should be produced by a purely random noise generator, which may not be accessed by any potential enemy or user.

Considering Figure 5, and in relation to the key provided as an example the following component parts may be
 15 seen:

1. From position 0 to 62, 63 bits which make up the definition of the LFSR binary coefficient polynomial.
- 20 2. 63-126, 63 bits which are initial state of the LFSR. It must be noted that if the preceding polynomial is primitive, the period P of the generated sequence does not depend on the initial status of the register. The specific sequence will
 25 be the same, but displaced for each seed.
3. 127-134, 8 bits which make up the horizontal dimension of the contour for the area of interest (Parameter T in Figure 2) in the G2000 for the first rotation of the sequence generator.
- 30 4. 135-142, 8 bits which make up the vertical dimension of the area of interest (Parameter R in Figure 2) in the G2000 for the first rotation of the sequence generator.
5. 143-149, 7 bits which read in different manners
 35 represent the parameters Ix, Iy, Rx, and Ry. It may be of interest to establish a minimum value such as

16 (for example) so that none of these parameters may have a zero value... notwithstanding, this is not strictly necessary.

6. 150-159, 10 bits which act as the horizontal location of the set of lines pole in relation to the left border of the contour (Parameter x in Figure 2), in the first rotation of the sequence generator.
7. 160-169, 10 bits which act as the vertical location of the set of lines pole in relation to the upper border of the contour (Parameter y in Figure 2) in the first rotation of the sequence generator.

It must be pointed out that parameters I_x , I_y , R_x and R_y are extracted from the same 7 bits (Fig. 2), as in order to ensure that by changing any bit of the key not even part of the message may be decrypted, it is necessary that these parameters are dependent, as if these four parameters were independent, and in some cases, in the first turnaround of the sequence generator, there would be sequence chains unchanged, as only one of the parameters has been modified; therefore it becomes necessary to ensure that the four parameters are modified; a good option is to extract them from the same bits which make up the key but in a different order, ensuring that this operation does not weaken the algorithm. This interdependence of parameters I_x , I_y , R_x , R_y must remain when these are extracted from the M-Sequence instead of K. Summing up, if a dependency is not established between parameters I_x , I_y , R_x , and R_y , it may happen that on attempting to decrypt a message changing the key in a single bit (or even in 7 bits), the beginning of the same appears as plaintext (not modified), which is not at all desirable.

Pursuant to the foregoing, a second flow chart example concerning the implementation of method E-G2000 as described under Figure 6 may now be considered, with similar stages to

those mentioned when describing the diagram of Figure 4, and the additions made in the figure are self-explanatory.

The pseudo-code used to carry it out would be similar to that
 5 of Table 1.

```
Call FilteredGenerator(BitsNumber)
```

```
Do while not EOF(File) = True
```

```
    Sequence parameters extraction
```

```
    Read and location of PlainArray (NxM)
```

```
    Call G2000(PlainArray, n)
```

```
    If FinalSequence = True then
```

```
        Call FilteredGenerator (BitsNumber)
```

```
    Endif
```

```
    Write and location of Clear Array in FinalFile
```

```
Loop
```

15 Where the FilteredGenerator() is a SubProcedure which
returns a certain number of bits (Bitsnumber) from the LFSP
sequence filtered by any of the procedures set out in Section
1 of the explanation to Figure 4, and G2000 is another
procedure which performs the graphic algorithm G2000 (or
20 G2000i) on the PlainArray, n times.

It is now appropriate to consider certain properties of
the E-G2000. If we consider an isolated cipher block, that is,
one of the plain data arrays which the E-G2000 uses in order
25 to perform the G2000 operation on them in combination with the
filtered generator and we plot it graphically we may easily
conclude that the cryptogram related to the same is the module
2 addition of that array with the array obtained by plotting
the G2000 in combination with the filtered sequence against an
30 empty background. Therefore, the strength of the encryption
method will depend of the degree of randomness of this last
array.

The industrial use of the methods and algorithms described in these Technical Specifications may be undertaken in many ways and specifically by the manufacture of:

- 5 1. Microcircuits to generate pseudo-noise sequence, based on any of the graphic algorithms, G1000, G2000, or G2000i.
 2. Encryption microcircuits capable of protecting data by means of algorithms E-G2000 or E-G2000i for communications, data storage, or both.
 - 10 3. Graphic cards with encryption facilities, by means of any of the abovementioned procedures or any combination of them, for communications, data storage, or both.
 4. Graphic Processing Unit (GPU) devices with encryption facilities based on any of the abovementioned algorithms
 - 15 or a combination of them, for communications, data storage, or both.
 5. Network cards, modem devices or in general communication devices capable of performing any of the foregoing algorithms, or a combination of them, for communications,
 - 20 data storage, or both.
 6. Peripheral devices intended for cryptographic functions by means of any of the foregoing algorithms or a combination of them.
- Or else, developing computer applications capable of
- 25 carrying out any of the foregoing algorithms or a combination of them.

In conclusion, by developing any machine (real or virtual) capable of undertaking an effective protection or transformation of data, whether in total or partial manner, by using any of the algorithms or methods based on the latter described up to this point.